

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
)
TAKAURA et al.)
)
Application Number: To Be Assigned)
)
Filed: Concurrently Herewith)
)
For: A SEMICONDUCTOR MEMORY DEVICE USING)
VERTICAL-CHANNEL TRANSISTORS)
)
Attorney Docket No.: HITA. 0451)

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

INFORMATION DISCLOSURE STATEMENT

Sir:

The above-referenced application is a Continuation of U.S. Serial No. 10/051,188, filed on January 22, 2002. It includes the same disclosure as U.S. patent application Serial No. 10/051,188.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application under 37 CFR Section 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

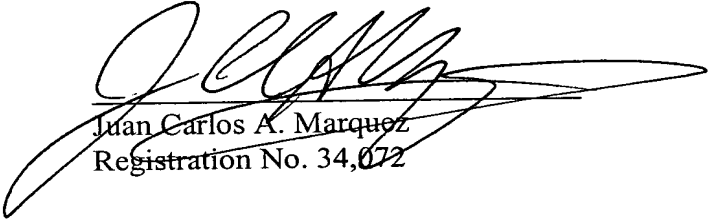
This Information Disclosure Statement is submitted with the above-captioned U.S. Continuation application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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October 28, 2003

Form PTO 1449 U.S. Department of Commerce Patent and Trademark Office Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER HITA.0451	SERIAL NUMBER To Be Assigned
	APPLICANT TAKAURA et al.	
	FILING DATE Concurrently Herewith	GROUP

U.S. Patent Documents

Examiner Initial	Cited in Parent	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	XX	6,309,930	10/30/2001	Goebel et al.			11/9/2000
	X	5,576,238	11/19/96	Fu			6/15/95
	X	5,341,327	8/23/94	Kuriyama			3/27/92
	X	5,198,683	3/30/93	Sivan			5/3/91
	X	6,229,161 B1	5/8/01	Nemati et al			6/5/98
	X	6,204,518 B1	3/20/01	Adan et al			1/19/00

Foreign Patent Documents

Examiner Initial	Cited in Parent	DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
							YES	NO
	X	9-232447	2/19/96	Japan			Abstract	X
	X	WO00/70622	5/14/99	PCT			Abstract	X

Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

EXAMINER		DATE CONSIDERED
<i>EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant</i>		

X = Filed by Applicant

XX = Cited by Examiner